

**Listing of Claims:**

Claim 1 (currently amended) A wafer carrier for carrying a wafer, comprising:

a transparent base;

a conducting layer positioned on a bottom surface of the transparent base; and

5 a bonding layer positioned on a top surface of the wafer carrier for bonding the wafer and the transparent base together;

wherein the wafer carrier is attracted by an electrostatic chuck via the conducting layer, and the electrostatic chuck is separated from the conducting layer before carrying the wafer.

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Claim 2 (original) The wafer carrier of claim 1, wherein the transparent base has dimensions similar to that of the wafer.

Claim 3 (original) The wafer carrier of claim 1, wherein the transparent base is a glass wafer.

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Claim 4 (original) The wafer carrier of claim 1, wherein the transparent base is a quartz wafer.

20 Claim 5 (cancelled)

Claim 6 (previously presented) The wafer carrier of claim 1, wherein the bonding layer is selected from the group consisting of double-sided tape, ultra violet tape, thermal sensitive tape, photo resist, and wax.

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Claim 7 (previously presented) The wafer carrier of claim 1, wherein the wafer is transferred and undergoes at least a semiconductor process.

Claim 8 (original) The wafer carrier of claim 7, wherein the semiconductor process is a double-sided process, and the wafer comprises at least an alignment mark.

5 Claim 9 (original) The wafer carrier of claim 8, wherein the conducting layer is a transparent conducting layer.

10 Claim 10 (original) The wafer carrier of claim 8, wherein the conducting layer is a non-transparent conducting layer having at least an exposed region corresponding to the alignment mark.

Claim 11 (original) The wafer carrier of claim 10, wherein the non-transparent conducting layer comprises a plurality of conducting patterns connected with each other.

15 Claim 12 (currently amended) A wafer carrier adapted for use in a double-sided process for carrying a wafer, comprising:

a transparent base;  
a conducting layer positioned on a bottom surface of the transparent base, wherein the wafer carrier is attracted by an electrostatic chuck via the conducting layer;  
and  
20 a bonding layer positioned on a top surface of the transparent base for bonding the wafer and the transparent base;  
wherein the electrostatic chuck is separated from the conducting layer before carrying the wafer.

25 Claim 13 (original) The wafer carrier of claim 12, wherein the transparent base has dimensions similar to that of the wafer.

Claim 14 (original) The wafer carrier of claim 12, wherein the transparent base is a glass

wafer.

Claim 15 (original) The wafer carrier of claim 12, wherein the transparent base is a quartz wafer.

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Claim 16 (original) The wafer carrier of claim 12, wherein the bonding layer is selected from the group consisting of double-sided tape, ultra violet tape, thermal sensitive tape, photo resist, and wax.

10 Claim 17 (previously presented) The wafer carrier of claim 12, wherein the wafer is transferred and undergoes the double-sided process.

Claim 18 (original) The wafer carrier of claim 17, wherein the wafer comprises at least an alignment mark.

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Claim 19 (original) The wafer carrier of claim 18, wherein the conducting layer is a transparent conducting layer.

20 Claim 20 (original) The wafer carrier of claim 18, wherein the conducting layer is a non-transparent conducting layer having at least an exposed region corresponding to the alignment mark.

Claim 21 (original) The wafer carrier of claim 12, wherein the non-transparent conducting layer comprises a plurality of conducting patterns connected with each other.

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Claim 22 (new) A method of carrying a wafer, comprising:  
providing a wafer carrier comprising:

- a transparent base;
  - a conducting layer positioned on a bottom surface of the transparent base; and
  - a bonding layer positioned on a top surface of the wafer carrier;
- using an electrostatic chuck to attract the wafer carrier via the conducting layer; and
- 5 using the bonding layer to bond a wafer and the transparent base together so as to carry the wafer.